

ABSTRACT

A method and system for signal-conditioning utilizing a signal-conditioning circuit is disclosed. An offset correction voltage can be applied to a noninverting input of a signal-conditioning circuit. A magnetoresistor half-bridge signal can be applied to an inverting input of the signal-conditioning circuit. A voltage can then be compensated at the noninverting input to drive an output voltage of the signal-conditioning circuit to an input voltage divided by a value of two by calibration, thereby permitting the signal-conditioning circuit to contain temperature compensation capabilities.

10026270-122101